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Application No.	Applicant(s)	•
10/626,594	NAKASHIMA ET AL.	
Examiner	Art Unit	
Mai-Huong Tran	2818	
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aminer.		
e been received. e been received in Application cuments have been received of this communication to file MENT of this application. itted. Note the attached EXA as reason(s) why the oath or set be submitted. Son's Patent Drawing Review of Samendment / Comment or 1.84(c)) should be written on the header according to 37 CF sit of BIOLOGICAL MATE	n No I in this national stage application from this national stage application from the requirement of the stage application from the requirement of the stage application from the office action of the drawings in the front (not the back) of a 1.121(d). ERIAL must be submitted. Note the	of
6. Interview Su Paper No./ Paper No./ 7. Examiner's 8. Examiner's 9. Other rid Nelms Patent Examiner	ummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowance	
	Examiner Mai-Huong Tran Pars on the cover sheet with (OR REMAINS) CLOSED in the properties of the properties of the application is set and MPEP 1308. Caminer. Inder 35 U.S.C. § 119(a)-(d) of the properties of the second of this communication to file deep received in Application. Inder 35 U.S.C. § 119(a)-(d) of the properties of this application. Inder 35 U.S.C. § 119(a)-(d) of the properties of the second of the properties of the second of the properties of the submitted. Inder 35 U.S.C. § 119(a)-(d) of the properties of	10/626,594

U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04) Application/Control Number: 10/626,594

Art Unit: 2818

DETAILED ACTION

Reason for allowance

The following is an examiner's statement of reason for allowance: None of the references of record teaches or suggests the claimed Semiconductor Memory Devices having a length of the memory cells in the extending direction of the word line that is longer than that in an extending direction of the bit lines, the bit lines are disposed above the access MOS transistors and the driver MOS transistors, the load elements are disposed above the bit lines, and the capacitive elements are disposed above the load elements.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

Any inquiry concerning this communication on earlier communications from the examiner should be directed to Mai-Huong Tran, (571) 272-1796. The examiner can normally be reached on Monday-Thursday from 8:00 AM to 6:30 PM. The examiner's supervisor, David Nelms can be reached on (571) 272-1787.

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The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR, Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mai-Huong Tran

David Nelms
Supervisory Patent Examiner
Technology Center 2800

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